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PIRATE: A Framework for Power/Performance Exploration of Network-On-Chip Architectures - all 2 versions »

G Palermo, C Silvano - Proc. of PATMOS, 2004 - Springer

... the results obtained by applying the proposed methodology to a case study: An on-chip **cryptographic accelerator** for high performance web server **systems**. ...

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The IBM PCIXCC: A new cryptographic coprocessor for the IBM eServer - all 6 versions »

TW Arnold, LP Van Doorn - IBM Journal of Research and Development, 2004 - research.ibm.com

... The CCF is a single-chip cryptographic coprocessor embedded in all IBM CMOS mainframe **systems**. IBM **cryptographic accelerator** card. ...

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An Adaptive Cryptographic Accelerator for IPsec on the Dynamically Reconfigurable Processor - all 2 versions »

Y HASEGAWA, S ABE, H MATSUTANI, K ANJO, T AWASHIMA ... - 2006 - search.ieice.org

... Summary: We propose a **cryptographic accelerator** for IPsec by using the NEC's ... an embedded

processor and DRP are integrated in a **System-on-a-Chip (SoC)** and ...

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Improving DES Coprocessor Throughput for Short Operations - all 4 versions »

M Lindemann, SW Smith - unix.org

... the value of programmability in a **cryptographic accelerator**---because without ... 2.

System Background. Our device is a multi-chip embedded module, packaged in a ...

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(book) Using a High-performance, Programmable Secure Coprocessor - all 12 versions »

S Smith, ER Palmer, S Weingart - 1997 - Springer

... device in the secure coprocessor family is the **cryptographic accelerator**. ... a secure coprocessor requires a host **system**. ... (For example, the PCMCIA, **chip-card**, and ...

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Cryptographic system enhancements for the IBM System z9 - all 3 versions »

TW Arnold, A Dames, MD Hocker, MD Marik, NA ... - IBM J. Res. & Dev - research.ibm.com

... to that provided by the PCI **cryptographic accelerator** (PCICA ... gate array (FPGA)/cryptographic **chip** for fast ... normal-path requests simultaneously, for **System** ...

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AD Keromytis, J.L Wright, T de Raadt - Proceedings of the USENIX Annual Technical Conference, 2003 - openbsd.dq.net.ua

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B Soudan - Xilinx White Paper WP123, August, 2000 - itc-electronics.ru

and the I/O timing (ie, getting data on and off chip). ... **System Bus Bridge** ...

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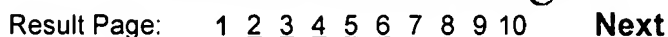
W Wong - Electronic Design, Jul, 2000 - elecdesign.com

four RISC processors on the **chip**. ... for an OC-192 signaling **system** known as ...

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G Bertoni - idea-group.com

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